IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:	10/546,004
Applicants:	Daisuke ADACHI et al.
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Title:	PLASMA DISPLAY PANEL
TC/A.U.:	2879
Examiner:	Anne M. Hines
Confirmation No.:	8651
Docket No.:	MAT-8729US

VERIFICATION OF A TRANSLATION

Commissioner for Patents
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S I R :

I, the below named translator, hereby declare that:

1. My name and post office address are as stated below.

2. That I am knowledgeable in the English language and in the attached Japanese language document, and I believe the attached English translation to be a true and complete translation of claim 2.

3. The document for which the attached English translation is being submitted is a page of International Application PCT/JP04/018850.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States

Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Date: October 10, 2007

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請求の範囲

1．少なくとも前面側が透明な一対の基板を基板間に放電空間が形成されるように対向配置し、前面側の基板には走査電極と維持電極とを備える表示電極と当該表示電極の間の非放電部に遮光部とを設け、背面側の基板には放電により発光する蛍光体層を設けたプラズマディスプレイパネルであって、前記表示電極を透明電極とバス電極とで構成し、前記バス電極を複数の電極層で構成するとともに前記電極層の少なくとも一層が抵抗率と膜厚との積が２Ωｃｍ^{2}以下の黒色層であり、前記遮光部が抵抗率が１×１０^{6}Ωｃｍ以上の黒色層であることを特徴とするプラズマディスプレイパネル。

2．少なくとも前面側が透明な一対の基板を基板間に放電空間が形成されるように対向配置し、前面側の基板には走査電極と維持電極とを備える表示電極と当該表示電極の間の非放電部に遮光部とを設け、背面側の基板には放電により発光する蛍光体層を設けたプラズマディスプレイパネルであって、前記表示電極を透明電極とバス電極とで構成し、前記バス電極を複数の電極層で構成するとともに前記電極層の少なくとも一層が抵抗率と膜厚との積が２Ωｃｍ^{2}以下の黒色層であり、前記遮光部が抵抗率が１×１０^{5}Ωｃｍ以上の黒色層であり、前記表示電極と前記遮光部とが電気的に絶縁されていることを特徴とするプラズマディスプレイパネル。

3．黒色層が少なくとも黒色顔料と導電材料とを含むことを特徴とする請求項１または２に記載のプラズマディスプレイパネル。

2. A plasma display panel having a pair of substrates with at least one transparent front side and positioned to face each other so that discharge spaces are formed between the substrates comprising:

a front substrate having display electrodes provided with scan electrodes and sustain electrodes, and a alight-shield formed on a non-discharge area between the display electrodes;

and

a rear substrate having phosphor layers to emit light by discharge, wherein

the display electrode comprises a transparent electrode and a bus electrode;

the bus electrode includes a plurality of electrode layers;

at least one of the electrode layers is composed of a black layer with a product of a resistivity and a layer thickness of not larger than 2 Ωcm^2 and the light-shield is composed of a black layer with a resistivity of not smaller than 1×10^5 Ωcm; and

the display electrode and the light-shield are electrically insulated.

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